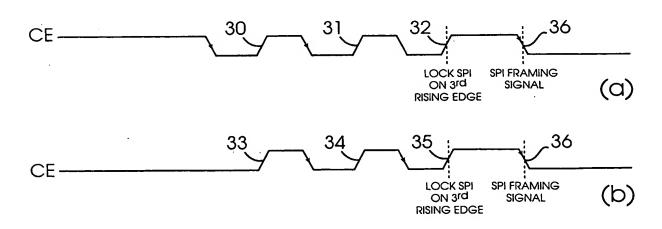
17



**Control Timing** 

of Signal Processing

Circuit

26

Fig. 2

Fig. 1

